

## VLSI IEEE PAPERS-2020

S.NO	TITLES	YEAR	CATEGORY
1	ER-TCAM: A Soft-Error-Resilient SRAM-Based Ternary Content-Addressable Memory for FPGAs	2020	TESTING
2	Design of Approximate Booth Squarer for Error-Tolerant Computing	2020	LOW POWER
3	On Fast and Exact Computation of Error Metrics in Approximate LSB Adders	2020	LOW POWER
4	Very Fast, High-Performance 5-2 and 7-2 Compressors in CMOS Process for Rapid Parallel Accumulations	2020	LOW POWER
5	Glitch-Optimized Circuit Blocks for Low-Power High-Performance Booth Multipliers	2020	LOW POWER
6	A Novel Time-Shared and LUT-Less Pipelined Architecture for LMS Adaptive Filter	2020	SIGNAL PROCESSING
7	Design Exploration of Energy-Efficient Accuracy-Configurable Dadda Multipliers With Improved Lifetime Based on Voltage Overscaling	2020	LOW POWER
8	A Hierarchical Scrubbing Technique for SEU Mitigation on SRAM-Based FPGAs	2020	TESTING
9	PCBChain: Lightweight Reconfigurable Blockchain Primitives for Secure IoT Applications	2020	CRYPTOGRAPHY
10	A New Logic Topology-based Scan Chain Stitching for Test-Power Reduction	2020	TESTING
11	Comparison and Extension of Approximate 4-2 Compressors for Low-Power Approximate Multipliers	2020	LOW POWER
12	A Scan Obfuscation Guided Design-for-Security Approach For Sequential Circuits	2020	CRYPTOGRAPHY
13	Block-based Carry Speculative Approximate Adder for Energy-Efficient Applications	2020	LOW POWER
14	Unified Hardware for High-Throughput AES-Based Authenticated Encryptions	2020	CRYPTOGRAPHY

15	A Modified Inexact Arithmetic Median Filter for Removing Salt-and-Pepper Noise from Gray-Level Images	2020	IMAGE PROCESSING
----	---	------	---------------------



CLAROZON  
TECHNOLOGIES  
*signs of innovation...*